

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Previously Presented) A resistor comprising:

a substrate having a width shorter than a length of said substrate;

a pair of electrodes disposed on said substrate, said pair of electrodes being disposed on both end portions of said substrate along a substantial portion of said width;

a resistor element disposed between said pair of electrodes, said resistor element including:

side sections, each of said side sections connected to each of said pair of electrodes along a substantial portion of a length of said pair of electrodes along said width, and

an S-shaped section disposed between said side sections,

a trimming portion formed within at least one of said side sections,

a space defined by said S-shaped section and said side sections,

highest levels of said substrate are lower at said trimming portion than at said space.

2. (Previously Presented) The resistor of claim 1, wherein a width of at least one of said side sections of said resistor is wider than a width of said S-shaped section.

3. (Cancelled)

4. (Previously Presented) The resistor of claim 1, wherein thickness of said side sections of said resistor element are twice as thick as said S-shaped section.

5. (Previously Presented) The resistor of claim 3, wherein a width of said side section of said resistor element where the side section extends to said S-shape section is wider than a width of said S-shaped section.

6. (Previously Presented) A method of manufacturing a resistor comprising the steps of:

forming a pair of electrodes on a substrate having a width shorter than a length of said substrate; and

forming a resistor element by printing between said pair of electrodes, said resistor element comprising i) side sections connected to each of said pair of electrodes along a substantial portion of a length of said pair of electrodes along a width of said substrate and ii) not more than a single S-shaped section disposed between said side sections, said S-shaped section being free of a trimming portion; and

trimming at least one of said side sections to adjust a resistance wherein, during trimming, a portion of said substrate is removed.

7-8. (Cancelled)

9. (Previously Presented) The resistor of claim 1, wherein said side sections are rectangular.

10. (Previously Presented) The method of manufacturing a resistor of claim 6, wherein said side sections are rectangular.

11. (Previously Presented) A resistor comprising:

a substrate having a width shorter than a length of said substrate;

a pair of electrodes disposed on said substrate, said pair of electrodes being disposed on both end portions of said substrate along said width;

a resistor element situated between said pair of electrodes, said resistor element including:

a pair of side sections, each of said side sections connected to a respective one of said pair of electrodes along a substantial portion of a length of said pair of electrodes along said width, and

an S-shaped section situated between said pair of side sections;

a trimming portion formed within at least one of said side sections;

a space defined by said S-shaped section and said side sections;

highest levels of said substrate are lower at said trimming portion than at said space;

wherein a width of said S-shaped section along said length of said substrate is less than a width of each of said side sections along said length of said substrate.

12. (Previously Presented) The resistor of claim 11, wherein each of said side section includes a respective trimming groove formed therein.

13. (Previously Presented) The resistor of claim 11, wherein said S-shaped section is free of trimming grooves.

14. (Previously Presented) The resistor of claim 12, wherein said S-shaped section is free of trimming grooves

15-16. (Cancelled).

17. (New) A resistor according to claim 1, wherein one of said trimming portions is closer to one of said electrodes than to said space.

18. (New) A resistor comprising:

a substrate having a width shorter than a length of said substrate;

a pair of electrodes disposed on said substrate, said pair of electrodes being disposed on both end portions of said substrate along a substantial portion of said width;

a resistor element disposed between said pair of electrodes, said resistor element including:

side sections, each of said side sections connected to each of said pair of electrodes along a substantial portion of a length of said pair of electrodes along said width, and

an S-shaped section disposed between said side sections,

trimming portions formed within said side sections,

a space defined by said S-shaped section wherein said S-shaped section loops about said space,

said space between said trimming portions,

highest levels of said substrate are lower at said trimming portion than at said space.

19. (New) A method of manufacturing a resistor comprising the steps of:

forming a pair of electrodes on a substrate having a width shorter than a length of said substrate; and

forming a resistor element by printing between said pair of electrodes to form said resistor element comprising i) side sections connected to each of said pair of electrodes along a substantial portion of a length of said pair of electrodes along a width of said substrate and ii) an S-shaped section disposed between said side sections, said S-shaped section looping around a space; and

trimming said side sections to adjust a resistance so that said space is between areas where said trimming occurs wherein, during trimming, a portion of said substrate is removed so that highest levels of said substrate are lower at said trimming portion than at said space.

20. (New) A resistor comprising:

a substrate having a width shorter than a length of said substrate;

a pair of electrodes disposed on said substrate, said pair of electrodes being disposed on both end portions of said substrate along said width;

a resistor element situated between said pair of electrodes, said resistor element including:

a pair of side sections, each of said side sections connected to a respective one of said pair of electrodes along a substantial portion of a length of said pair of electrodes along said width, and

an S-shaped section situated between said pair of side sections,

trimming portions formed within said side sections,

a space defined by said S-shaped section wherein said S-shaped section loops about said space,

said space between said trimming portions,

highest levels of said substrate are lower at said trimming portion than at said space.

wherein a width of said S-shaped section along said length of said substrate is less than a width of each of said side sections along said length of said substrate.

21. (New) The resistor of claim 1, wherein said substantial portion of said length is a majority of said length.

22. (New) The method of claim 6, wherein said substantial portion of said length is a majority of said length.

23. (New) The resistor of claim 11, wherein said substantial portion of said length is a majority of said length.